## What the invention claimed is:

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- 1. A KVM switch comprising an electrically insulative housing and a control circuit mounted inside said housing and adapted to receive a plurality of computers and computer peripheral apparatus including a keyboard, a mouse, and a display, said control circuit comprising a program adapted to selectively control output signal from one of said computers to said display subject to the operation of pre-set buttons of the keyboard and mouse been connected to said control circuit.
- 2. The KVM switch as claimed in claim 1, wherein the keyboard which is connected to said control circuit in said housing has pre-set button means adapted to switch on/off the mouse being connected to said control circuit in said housing.
- 3. The KVM switch as claimed in claim 1, wherein the keyboard which is connected to said control circuit in said housing has pre-set button mans for switching on/off the mouse being connected to said control circuit in said housing, and pre-set button means for control switching between the computers and the display.
- 4. The KVM switch as claimed in claim 1, wherein said 20 control circuit in said housing comprises:
  - a microprocessor, which has a control program adapted to connect a keyboard, a mouse, and a plurality of computers;

an OSD (ON SCREEN DISPLAY) control circuit connected

to said microprocessor and adapted to select display picture subject to the program of said microprocessor;

a filtering switching circuit connected to said OSD control circuit and a display and adapted to filter the picture not selected by said OSD control circuit and to switch to the selected signal for enabling the selected signal to be displayed on the display connected thereto;

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a keyboard control circuit connected to said microprocessor and the computers being connected to said microprocessor to serve as an interface between said microprocessor and the connected computers and to examine and convert the output signal of said keyboard into a format readable to said microprocessor:

a mouse control circuit connected to said microprocessor and the computers at said microprocessor to serve as an interface between said microprocessor and the connected computers and to examine and convert the output signal of said mouse into a format readable to said microprocessor; and

a VGA control circuit connected to said filtering switching circuit the computers at said microprocessor to serve as an interface between said filtering switching circuit the connected computers and to examine and convert the output signal of the connected computers into a format readable to said filtering switching circuit.